### DISCRETE SEMICONDUCTORS

## DATA SHEET

**PEMH10**; **PUMH10** NPN/NPN resistor-equipped transistors; R1 = 2.2 kΩ, R2 = 47 kΩ

Product specification Supersedes data of 2001 Oct 22 2003 Oct 20





## NPN/NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = 47 k $\Omega$

PEMH10; PUMH10

#### **FEATURES**

- Built-in bias resistors
- · Simplified circuit design
- Reduction of component count
- · Reduced pick and place costs.

#### **APPLICATIONS**

- · Low current peripheral driver
- Replacement of general purpose transistors in digital applications
- · Control of IC inputs.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V <sub>CEO</sub>	collector-emitter voltage	_	50	V
I <sub>O</sub>	output current (DC)	_	100	mA
TR1	NPN	_	_	_
TR2	NPN	_	_	_
R1	bias resistor	2.2	-	kΩ
R2	bias resistor	47	_	kΩ

#### **DESCRIPTION**

NPN/NPN resistor-equipped transistors (see "Simplified outline, symbol and pinning" for package details).

#### PRODUCT OVERVIEW

TYPE	PACKAGE		MARKING CODE	PNP/PNP	NPN/PNP
NUMBER	PHILIPS	EIAJ	WARKING CODE	COMPLEMENT	COMPLEMENT
PEMH10	SOT666		10	PEMB10	PEMD10
PUMH10	SOT363	SC-88	H*0 <sup>(1)</sup>	PUMB10	PUMD10

#### Note

- 1. \* = p: Made in Hong Kong.
  - \* = t: Made in Malaysia.
  - \* = W: Made in China.

#### SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	CIMPLIFIED OUTLINE AND CVMPOL		PINNING
TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PIN	DESCRIPTION
PEMH10	6 5 4	1	emitter TR1
PUMH10		2	base TR1
	R1 R2	3	collector TR2
	TR2	4	emitter TR2
		5	base TR2
		6	collector TR1
	1 2 3		
	Top view MHC650		
	MHC650		

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#### **ORDERING INFORMATION**

TVDE NUMBER		PACKAGE	
TYPE NUMBER NAME		DESCRIPTION	VERSION
PEMH10	_	plastic surface mounted package; 6 leads	SOT666
PUMH10	_	plastic surface mounted package; 6 leads	SOT363

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS			UNIT
Per transist	or		<u>'</u>	•	
V <sub>CBO</sub>	collector-base voltage	open emitter	_	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	50	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	10	V
V <sub>I</sub>	input voltage				
	positive		_	+12	V
	negative		_	<b>-</b> 5	V
Io	output current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
	SOT363	note 1	_	200	mW
	SOT666	notes 1 and 2	_	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C
Per device		•		•	
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C			
	SOT363	note 1	_	300	mW
	SOT666	notes 1 and 2	_	300	mW

#### **Notes**

- 1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
- 2. Reflow soldering is the only recommended soldering method.

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## NPN/NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = 47 k $\Omega$

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#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per transist	or			
R <sub>th j-a</sub>	thermal resistance from junction to ambient	T <sub>amb</sub> ≤ 25 °C		
	SOT363	note 1	625	K/W
	SOT666	notes 1 and 2	625	K/W
Per device				
R <sub>th j-a</sub>	thermal resistance from junction to ambient	T <sub>amb</sub> ≤ 25 °C		
	SOT363	note 1	416	K/W
	SOT666	notes 1 and 2	416	K/W

#### **Notes**

- 1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
- 2. Reflow soldering is the only recommended soldering method.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT				
Per transis	Per transistor									
I <sub>CBO</sub>	collector-base cut-off current	V <sub>CB</sub> = 50 V; I <sub>E</sub> = 0	_	_	100	nA				
I <sub>CEO</sub>	collector-emitter cut-off current	$V_{CE} = 30 \text{ V}; I_{B} = 0$	_	_	1	μΑ				
		$V_{CE} = 30 \text{ V}; I_{B} = 0; T_{j} = 150 ^{\circ}\text{C}$	_	_	50	μΑ				
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0$	_	_	180	μΑ				
h <sub>FE</sub>	DC current gain $V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}$		100	_	_					
V <sub>CEsat</sub>	collector-emitter saturation voltage	$I_C = 5 \text{ mA}; I_B = 0.25 \text{ mA}$	_	_	100	mV				
$V_{i(off)}$	input-off voltage	$I_C = 100 \mu\text{A};  V_{CE} = 5 \text{V}$	_	0.6	0.5	V				
V <sub>i(on)</sub>	input-on voltage	$I_C = 5 \text{ mA}; V_{CE} = 0.3 \text{ V}$	1.1	0.75	_	٧				
R1	input resistor		1.54	2.2	2.86	kΩ				
R2 R1	resistor ratio		17	21	26					
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	_	2.5	pF				

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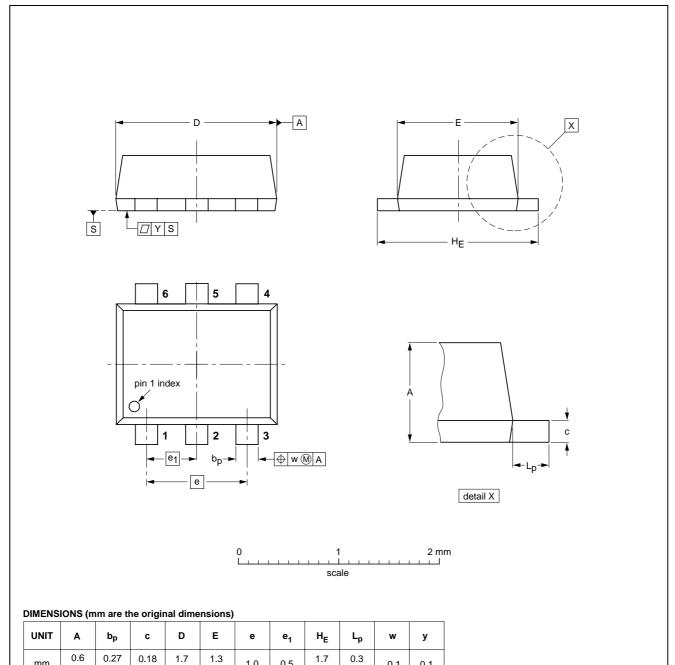
## NPN/NPN resistor-equipped transistors; $R1 = 2.2 \text{ k}\Omega$ , $R2 = 47 \text{ k}\Omega$

PEMH10; PUMH10

#### **PACKAGE OUTLINES**

Plastic surface mounted package; 6 leads

**SOT666** 



OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT666						<del>-01-01-04</del> 01-08-27

1.5

0.1

1.0

0.5

2003 Oct 20 5

mm

0.17

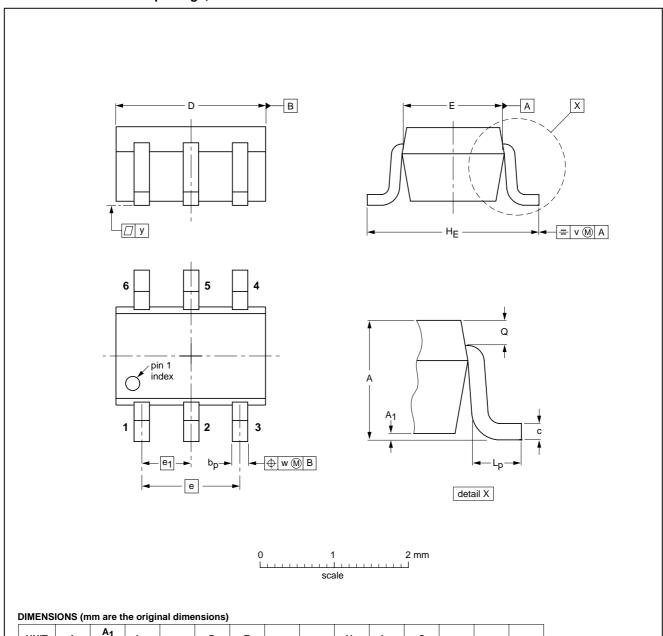
0.08

# NPN/NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = 47 k $\Omega$

PEMH10; PUMH10

#### Plastic surface mounted package; 6 leads

**SOT363** 



UNIT	Α	A <sub>1</sub> max	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	٧	w	у
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE		REFER	ENCES	EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT363			SC-88			97-02-28

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### NPN/NPN resistor-equipped transistors; R1 = 2.2 k $\Omega$ , R2 = 47 k $\Omega$

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#### **DATA SHEET STATUS**

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS(2)(3)	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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#### **Notes**

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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
- 3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

#### **DEFINITIONS**

**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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Printed in The Netherlands

R75/02/pp8

Date of release: 2003 Oct 20

Document order number: 9397 750 11874

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